A Velocity-Overshoot Subthreshold Current Model for Deep-Submicrometer MOSFET Devices

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ABSTRACT

In this paper, a new theoretical approach to submicrometer MOSFET subthreshold current modeling is presented. The diffusion and drift currents are calculated, respectively. The effect of velocity overshoot on subthreshold current is investigated. Comparison with MEDICI simulation results verifies the model.

1 INTRODUCTION

Advances in VLSI fabrication technology drive the devices towards deep-submicrometer dimensions. For many kinds of special effect such as velocity overshoot effect, short channel effect, drain-induced barrier lowering etc., submicrometer MOSFETs exhibit different characteristics compared to conventional ones. Thus unconventional approach has to be used in simulating the performance of submicrometer devices.

Subthreshold characteristic of MOSFETs is quite important for low-voltage, low-power applications, such as when MOSFETs are used as switches in digital logic and memory applications, because the subthreshold region describes how the switch turns on and off. In the subtheshould region, the diffusion current is usually expected to be dominant, because the depletion charge is much larger than the inversion charge. But for submicrometer MOSFET, the contribution of the drift current to the total current can not be neglected, because of the stronger lateral electrical field in shorter channel device.

In this paper, an analytical model of subthreshold current of short-channel MOSFETs is presented, in which the diffusion and drift currents are calculated separately, particularly velocity overshoot and velocity saturation are taken into consideration. The calculation results of this model are compared with MEDICI data for verification.

2 MODEL DERIVATION

The schematic cross section of the MOSFET is shown in Fig.1.

The drift velocity in a homogeneous low-field $v_h$ is given as

$$ v_h = -\frac{\mu E}{1 + E/E_s} \left( |E| < E_s \right) \quad (1) $$

where $E_s$ is the electric field at which the carrier velocity reaches saturation, $\mu$ is the low-field mobility.

The velocity overshoot is closely related to the field gradient in channel of MOSFET. The velocity in an inhomogeneous channel electric field $E$ can be expressed approximately as

$$ v = v_h (1 + \frac{\theta(E)}{E} \frac{dE}{dy}) \quad (2) $$

where $\theta(E)$ is the coefficient for the field-gradient effect which is given by

$$ \theta(E) = \frac{4}{3} v_s \tau_e \frac{2 + E/E_s}{1 + E/E_s^2} \frac{E}{E_s} \left( |E| < |E_s| \right) \quad (3) $$

where $\tau_e$ is the energy relaxation time, $v_s$ is the carrier saturation velocity which is given as

$$ v_s = -\frac{\mu E_s}{2} \quad (4) $$

The equation of channel surface potential $V_s$ (the inversion charges are neglected in subthreshold condition) is

$$ \eta = -\varepsilon + \frac{\eta}{X q N} \frac{dV_{GS}}{dy} + \frac{V_{GS} - V_{fb} - V_s(y)}{T_{ox}} \quad (7) $$

where $\varepsilon$ and $\varepsilon_{OX}$ are the permittivity of Si and SiO$_2$, $V_{GS}$ is the gate-source voltage, $V_{fb}$ is the flatband voltage, $T_{ox}$ is the gate-oxide thickness, $X_{dep}$ is the...
depletion layer thickness at strong inversion with zero drain bias, which is equal to \( \sqrt{\frac{4\varepsilon_S kT}{q N_A}} \), \( \psi_F \) is Fermi potential. The average depletion layer thickness at weak inversion is given as \( X_{\text{dep}}/\eta \), where \( \eta \) is the fitting parameter. \( N_A \) is the substrate uniform doping.

The solution to (7) under the boundary conditions: \( V_{C}(0) = V_{bi} \) and \( V_{C}(L) = V_{DS} + V_{bi} \) (the substrate potential is taken as ground) is

\[
V_{C}(y) = (V_{bi} + V_{DS} - V_{gs} + V_{0}) \frac{\sinh(y/L)}{\sinh(L/L)}
\]

\[
+ (V_{bi} - V_{gs} + V_{0}) \frac{\sinh[(L-y)/L]}{\sinh(L/L)} + V_{gs} - V_{0}
\]

in (8), \( V_{bi} = V_{i}\text{n} + \frac{q N_A X_{\text{dep}} T_{\text{OX}}}{\eta e_{\text{OX}}} \), \( V_{i}\text{n} \) is the PN junction built-in potential, \( \eta \) is defined as

\[
\eta = \frac{e_{\text{OX}} T_{\text{OX}} X_{\text{dep}}}{e_{\text{OX}} \gamma}
\]

Assuming the channel potential has a minimum at \( y_{\text{min}} \), then

\[
\frac{dV_{C}}{dy}|_{y=y_{\text{min}}} = 0
\]

By solving the Poisson’s Equation, the channel surface charge \( Q_{s} \) can be obtained as

\[
Q_{s} = -\frac{\sqrt{2e_{S}kT}}{qL_{D}} \Gamma\frac{q}{KT} V_{i}\text{n},(\psi_{n} - \psi_{F}),\frac{n_{p0}}{p_{0}})
\]

(11) where the surface potential \( V_{i}\text{n} \) and the quasi-Fermi potential \( \psi_{n} \) are the functions of the position \( y \) along the channel. And

\[
L_{D} = (\frac{KTE_{S}}{q})^{1/2}
\]

(12a)

\[
\psi_{F} = (kT/q) \ln(N_{A}/n_{i})
\]

(12b)

\[
\Gamma = \frac{q}{KT} V_{i}\text{n},(\psi_{n} - \psi_{F}),\frac{n_{p0}}{p_{0}})
\]

(12c)

\[
[\frac{q}{KT} V_{i}\text{n},(\psi_{n} - \psi_{F}),\frac{n_{p0}}{p_{0}})
\]

\[
= \{e^{\frac{-q}{KT} V_{i}\text{n},(\psi_{n} - \psi_{F}),\frac{n_{p0}}{p_{0}})} - e^{\frac{-q}{KT} V_{i}\text{n},(\psi_{n} - \psi_{F}),\frac{n_{p0}}{p_{0}})} \}
\]

(12d)

The depletion charge \( Q_{n} \) is given as

\[
Q_{n} = -(2qN_{A}e_{S} V_{i}\text{n},e_{S})^{1/2}
\]

(13)

In subthreshold region, from (11) and (13) the channel inversion charge \( Q_{i} \) can be approximately expressed as

\[
Q_{i} = Q_{s} - Q_{n}
\]

(14)

\[
= \sqrt{2e_{S}kT} N_{A} \frac{q}{KT} e^{(V_{i}\text{n},-2\psi_{n}-\delta_{n})q/kT}
\]

In order to get \( \delta_{n} \), Gauss’s law is used. The height of the Gaussian box is the depth of source and drain diffusion regions, and one of its edges is fixed at the point of \( y \). Then one can get the following equation [5]

\[
E_{s} x_{j} - E_{y} y_{j} - \frac{e_{s}}{e_{S}} \int_{y}^{V_{gs} - \delta_{n}(y)} dy
\]

(15)

\[
= -\frac{q(N_{A} + N_{i})}{e_{S}} x_{j}(y_{s} - y)
\]

where \( N_{i} \) is the bulk density of inversion charge, \( E_{y}(y) \) is the channel lateral electric field, and \( E_{s} \) is the electric field at which carrier velocity saturates. Differentiating (15) with respect to \( y \), one obtains

\[
\frac{dE_{s}(y)}{dy} = \frac{e_{s} V_{GS} - \delta_{n}(y)}{e_{S}} T_{ox}
\]

(16)

\[
= -\frac{q(N_{A} + N_{i})}{e_{S}} x_{j}(y_{s} - y)
\]

When \( y = y_{s} \), and the channel lateral electric field near source end is assumed to increase linearly in order to simplify (15) and (16), [5] (16) becomes

\[
E_{s} x_{j} - \frac{e_{s} V_{GS} - \delta_{n}(y_{s})}{e_{S}} T_{ox}
\]

(17)

\[
= -\frac{q(N_{A} + N_{i})}{e_{S}} x_{j}(y_{s} - y)
\]

(18)

Equation (18) can be rewritten as

\[
\frac{d^{2}\delta_{n}(y)}{dy^{2}} = \frac{\delta_{n}(y) - \delta_{n}(y_{s}) - E_{s}}{\lambda^{2}}
\]

(19)

Apparently, the boundary conditions of (19) are as follows

\[
1. \frac{d\delta_{n}(y)}{dy}|_{y=y_{s}} = -E_{s}
\]

(19a)

\[
2. \delta_{n}(y)|_{y=0} = 0
\]

(19b)

\[
3. \delta_{n}(L) = V_{DS}
\]

(19c)

With these boundary conditions, (19) can be solved as

\[
\delta_{n}(y) = a e^y + b e^{-y} + \frac{E_{s}}{y_{s}} + \delta_{n}(y_{s})
\]

(20)

where

\[
a = \frac{V_{DS}}{e^{L/\lambda} - 1}
\]

(21)

\[
b = \frac{\lambda E_{s}(e^{L/\lambda} - 1)}{e^{L/\lambda} - e^{-y_{s}/k} + e^{(y_{s} - L)/k} - V_{DS} e^{y_{s}/k}}
\]

(22)
Combining (19a), (20), (21), (22), $y_s$ can be obtained. From (19b) and (20), $\delta_n(y_s)$ has the following relation

$$\delta_n(y_s) = \frac{E_s \lambda^2}{y_s} - a - b \quad (23)$$

With the expression of inversion charge, the drift and diffusion currents of a MOSFET can be written as

$$I_{drift}(y) = -WQ_n(y)\nu(y)$$

$$= W\left[\frac{2q\epsilon_s N_A}{2\sqrt{V_s(y)}} - (V(y) - 2\phi_n - \phi_s)\right] - \frac{\mu_{eff}E_s(y)}{1 + E_s(y)/E_c}$$

$$I_{diff} = -D_n W \frac{dQ_l(y)}{dy} = -K\mu_{eff} W \frac{dQ_l(y)}{dy} \quad (24)$$

$$I_{diff} = -D_n W \frac{dQ_l(y)}{dy} = -K\mu_{eff} W \frac{dQ_l(y)}{dy} \quad (25)$$

$$\frac{dV_s(y)}{dy} = \frac{(V_{bi} + V_{DS} - V_{SL}) \cosh(y/L)}{1 - \frac{L-y}{L} \sinh(L/1)} \quad \frac{d\delta_n(y)}{dy} = \frac{a}{\lambda} e^{y/\lambda} - \frac{b}{\lambda} e^{-y/\lambda} \quad (26)$$

Integrating (24) and (25) from 0 to $y_s$, the channel drift and diffusion currents can be easily obtained. The device drain current is the sum of the drift and diffusion currents.

Because the effect of channel length modulation is apparent in deep-submicrometer devices, the effective channel length in [7] is applied to replace of the channel length ($L$) in the above equations:

$$L_{eff} = \exp(qV_{diss} / KT) \int_{0}^{L} \exp(-qV_s(y) / KT) dy \quad (29)$$

3 RESULTS AND DISCUSSION

The physical and device structure parameters used in the simulation are listed in Table 1.

In order to observe the distribution of surface potential along the channel, the calculated surface potentials for three channel lengths (0.25µm, 0.5µm and 1µm) at $V_{DS}=4V$ and $V_{GS}=0.3V$ are plotted together in Fig.2. It is apparent that the surface potential is almost constant for long channel, but has a large variation in short channel devices. This shows that it is necessary to use various surface potentials for deep-submicrometer device simulation.

Fig.3 (a, b) shows the of calculated drain current vs gate voltage at $V_{DS}=2V$, 4V, and compares with MEDICI simulation. The parameter $\eta$ (=1) in the model is determined as 1 by fitting to the MEDICI data at $V_{DS}=4V$ and $V_{GS}=0.1V$. The calculated results agree well with MEDICI in subthreshold region ($V_{th}=0.37V$) at different $V_{DS}$. The reason for the increasing discrepancy with MEDICI results above $V_{GS}=0.4V$ is that the model does not include inversion charge. However this should not influence the device subthreshold performance.

To clarify the carrier velocity overshoot effect on device subthreshold characteristics, we compare the calculated drain currents with those ignoring the overshoot effect at $V_{DS}=2V$ and 4V, as shown in Fig.4 (a, b). Without overshoot, the effective mobility in high field is replaced with the low field mobility. At $V_{DS}=2V$, the difference between the drain currents with and without overshoot is quite small. But when $V_{DS}$ becomes larger, overshoot effect becomes very obvious. At low drain bias, the lateral electric field near the source is small, and the velocity overshoot effect is not significant. At large drain voltage, the electric field near source increases, the carrier can gain much more energy in the channel, and velocity overshoot becomes more apparent. High lateral electric field in the channel is the basic factor of overshoot effect. Both short channel and high drain bias are the conditions under which this effect appears.

4 CONCLUSIONS

This paper presents a new physics-based subthreshold current model for deep-submicrometer MOSFETs. In the model, an effective mobility including carrier velocity overshoot is used to replace the low electric field mobility. The simulation results show that overshoot effect is very important at short channel and high drain bias. Accurate channel surface potential model is essential for drain current simulation because it is no longer a constant in short channel. Gauss Law is adopted to calculate the surface potential, partial voltage drop of drain bias and the quasi-Fermi level at every point in the channel. The weak inversion charge model is developed by solving Poisson equation involving the expressions of surface potential and quasi-Fermi level. Drift current cannot be neglected in short channel devices, especially at high drain voltages. The calculated results of this model agrees well with numerical data in the subthreshold region.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
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<tr>
<td>Energy relaxation time $\tau_s$</td>
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<tr>
<td>Carrier velocity saturation</td>
<td>$1\times10^6$ V/m</td>
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<td>Electric field $</td>
<td>E_s</td>
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<td>Gate length $L_g$</td>
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<td>Depth of source and drain diffusion $x_i$</td>
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<td>Uniform substrate doping $N_A$</td>
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<td>Uniform doping channel length $L$</td>
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<tr>
<td>Thickness of gate oxide layer $T_{ox}$</td>
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<tr>
<td></td>
<td>4.5nm</td>
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</tbody>
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Table 1: Physical and Device Structure Parameters
Fig. 2: Channel surface potential vs. channel position y for three gate lengths

Fig. 3: Calculated and MEDICI simulated drain current $I_{DS}$ as a function of gate voltage $V_{GS}$
(a): $V_{DS}=2V$ (b): $V_{DS}=4V$

Fig. 4: Calculated drain current $I_{DS}$ as a function of gate voltage $V_{GS}$ with or without overshoot
(a): $V_{DS}=2V$ b: $V_{DS}=4V$

REFERENCES


