



Welcome to WCM-MSM2003

WCM-MSM2003

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The second
Workshop on Compact Modeling

in collaboration with
**The 6th International Conference on
Modeling and Simulation of Microsystems**

San Francisco, CA, USA
February 25–27, 2003

Acknowledgments and Credits

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- **All the invited speakers/panelists** — your active participation, time and efforts in preparation of your presentations
- **MSM/NanoTech2003 organizers**
Matthew Laudon, Bart Romanowicz, Sarah Wenning
- **Participation of contributed poster papers as well as the audience**
- **Organizer: coordination.** WCM will not be possible without the participation of all the people mentioned above.

Overview and Goal of WCM

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Overview

- First WCM was initiated last year at the 5th MSM in Puerto Rico
- Participation of 23 invited speakers, panel discussion, and tutorials

Objective

- To create a *truly open forum* for discussion among experts in the compact-modeling field as well as feedback from technology developers, circuit designers, and CAD tool vendors

Scope

- Covers ***compact models for circuit simulation***, which is centered at the mainstream MOS intrinsic models and extended to SOI, bipolar, RF, noise, interconnect, extrinsic, statistical, numerical-based, and reliability models

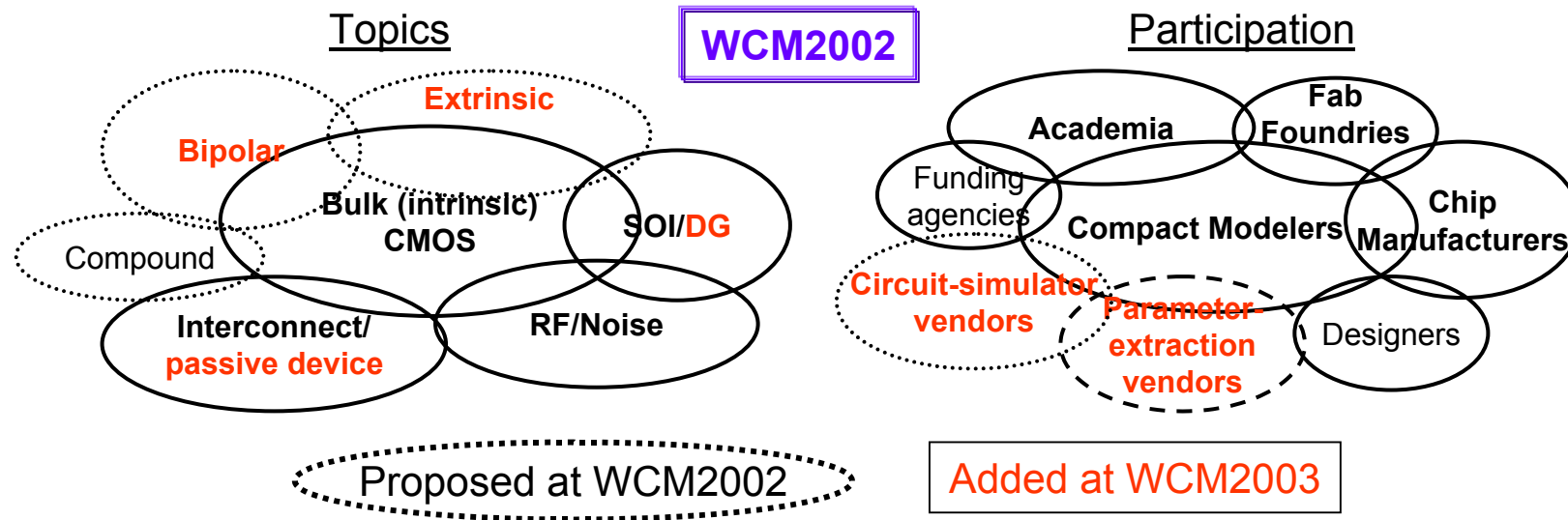
Goal

- To bring different walks of people to walk together on the same road (compact model developers, process engineers, device physicists, and circuit designers from universities, research institutions, chip manufacturers, wafer fabs, fabless companies, consulting firms, parameter-extraction tool and circuit-simulator vendors)

WCM2003 — Topics/Participation

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Announcement

Call for a Meeting — feedback on WCM and suggestions for future WCM

- **Wednesday, 17:15 – 18:00**, Merced Room (after Poster Oral Briefing)
- All invited speakers/panelists as well as participants are welcome!

WCM2003 Highlights

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☐ Represented models/simulators /interfaces (15)

- ACM
- ADMS
- BSIM
- EKV
- HICUM
- HiSIM
- Mextram
- MOS11
- SEQUEL
- SP
- UFDG
- USIM
- VBIC
- Verilog-A
- Xsim

☐ Participation by countries/regions (13)

- Belgium
- Brazil
- Germany
- Hong Kong
- India
- Japan
- Korea
- Poland
- Singapore
- Switzerland
- Taiwan
- The Netherlands
- USA

☐ Participation by affiliations (34)

Academia (17)

- EPFL
- Hiroshima
- HKUST
- IIT-Bombay
- KAIST
- NTU
- Penn State Univ
- Santa Clara Univ
- Stanford Univ
- Stony-Brook
- TU Lodz
- UC Berkeley
- UC San Diego
- UFSC
- Univ Antwerp
- Univ Florida
- UT Dresden

Industry (17)

- Agere
- AMD
- AMS
- Cadence
- CSEM
- CSM
- IBM
- Intel
- LSI Logic
- Mentor
- Motorola
- NIAIST
- NNDL
- Philips
- Synopsys
- Tiburon
- TSMC

Invited Papers, Forum, Panel Discussion, Contributed Posters, and Tutorials

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□ Invited papers (22)

(3-day: Tue., Wed., Thur.)

Topics (9)

- Compact modeling and design
- Bulk MOS intrinsic models
- SOI/double-gate MOS models
- Bipolar models
- RF/noise models
- Interconnection/passive device models
- Extrinsic/parasitic element models
- Model parameter extraction and optimization
- Model-simulator interface and standardization

□ Forum (Tue. 4:30 – 6:00pm)

Model development – industry requirement dialogue

“The Role of Compact Model in the Fab and Fabless Business”

□ Panel discussion

(Tue. 7:30 – 9:00pm)

Panelists (8)

Academia:

- Stanford
- UC Berkeley

Chip industry:

- Intel
- LSI Logic
- Philips

EDA vendors:

- Cadence
- Mentor Graphics
- Synopsys

□ Contributed posters (14)

(Wed. 4:00pm –)

- AMD
- IBM
- NIAIST
- NNDL
- NTU
- Penn State Univ
- TU Lodz
- UC Berkeley
- Univ Antwerp

□ Tutorials (2)

(Thur. 2 – 4:30pm)

- Mextram (Philips)
- SEQUEL (IIT-Bombay)



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Thank you for your participation!

**Hope to see you all
next year in Boston**

(March 7–11, 2004)

(Visit <http://www.ntu.edu.sg/home/exzhou/WCM/> for past/future WCM events)